

## CURRICULUM VITAE



1. Name :Dr. T. Venkateswarlu  
2.Designation :Professor of ECE  
3.Address for communication : Professor of ECE  
Dept. of EEE  
College of Engineering  
S.V.University  
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A.P., INDIA  
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Mobile: 9490181145  
4. Date of birth : 16<sup>th</sup> July, 1957  
5. Academic Qualifications :

Degree	Specialization	Institution/University	Year of passing	Class
Ph.D.	Digital Signal Processing	I.I.T., Madras, India	1990	-
M.Tech.	Electronic Instrumentation & Comm. Systems	S.V. University College of Engg., Tirupati, India	1981	1 <sup>st</sup> Class with Distinction
B.Tech.	Electronics & Comm. Engg.	-DO-	1979	1 <sup>st</sup> Class
Intermediate	Maths's., Physics, Chemistry etc.	Board of Int. Edn., Hyderabad, India	1974	1 <sup>st</sup> Class
S.S.C	-DO-	Board of Sec. Edn., Hyderabad, India	1972	-DO-

Degree Obtained	Title of the Thesis	Fellowships
Ph.D.	Realization of Multidimensional Digital Filters	Quality Improvement Program Fellowship from Ministry of Technical Education, New Delhi, India
M.Tech.	Utilization of Personnel computers in SLV Real Time Configuration	Fellowship from University Grants Commission
B.Tech	Timers, Waveform Generators and phased Locked Loops	National Scholarship

6. Total Experience : Thirty four years of teaching experience in graduation (M.Tech)., under graduation (B.Tech.) course at the University level.

7. Experience Details  
(a) Teaching

Position	From	To	Institution/University
Professor	Sept., 2008	Till to date	S.V.University,College of Engg., Tirupati,A.P., India
Professor, HOD & Dean	July, 2007	Aug., 2008	Sree Vidyanikethan Engineering College, Sree Sainath Nagar, A.Rangampet
Professor	Sep., 2000	July, 2007	S.V.University College of Engg., Tirupati,A.P., India
Associate Professor	July, 2000	Sept., 2000	-Do-
Associate Professor	Sept., 1992	July , 1997	-DO-
Senior Lecturer	July, 1997	July, 2000	Multimedia University, Melaka, Malaysia
Assistant Professor	March, 1982	Sept., 1992	S.V.University College of Engg., Tirupati,A.P., India
Lecturer	Dec., 1981	Feb., 1982	K.S.R.M. College of Engg., Cuddapah, AP,India
Teaching Asst.	Sept., 1981	Dec., 1981	-DO-

(b) Research:

Position	From	To	Institution/University
Research Scholar (Quality Improvement Program)	July, 1986	July, 1989	I.I.T., Madras, India

8. Research area of interest

: Design, Realization of one &  
Multidimensional digital filters and transfer  
functions.

9.Research Guidance(Ph.D.)

: Number of Students Registered #4  
Area of work: Digital Signal Processing

10.Publications

: List attached

11.Project Guidance

: Number of B. Tech. Projects guided # 60  
Number of M. Tech. Projects guided # 35

12. Administrative Positions Held : (i) Worked as; HOD for the Dept. of EEE & ECE, CPC member, WBC member, all at S.V. University, Tirupati during 2005-2007.  
(ii) Working as officer in charge for college Book- Bank, SVUCE, Tirupati.  
(iii) Worked as Dean (Electrical Sciences) & HOD at SVEC, A.Rangampeta.  
(iv) Subject expert for faculty selections for universities of SVU, AU, OU, JNTU.
13. Additional academic Duties : (i) Worked as Chairman, Board of Studies, Dept. of EEE & ECE, S.V. University, Tirupati.  
(ii) Working as Chairman, Board of Studies, Dept. of ECE, S.V. University, Tirupati.  
(iii) Worked as Dean (Electrical Sciences) & HOD, SVEC, Tirupati.  
(ii) Member, Board of Studies, ECE, Sri Padmavathy Mahila Viswavidyalayam, Tirupati.  
(iii) Member, Board of Studies, ECE, V.R. Siddhartha Engineering College, Vijayawada.  
(iv) Member, Board of Studies, ECE, K.L. College of Engineering, Vaddaswaram, A.P.  
(v) Ph.D. thesis adjudicator of a number of Universities
14. Countries Visited : Singapore, Malaysia.

# **LIST OF PUBLICATIONS**

## **International Journal Papers**

- [1] **T.Venkateswarlu**, “Direct Canonic Synthesis Of All-Pass Digital Filter Structures”, IEEE Trans. Circuits & Syst. Part I, Fundamental theory and appl., vol.46, No.12, pp.1495-1497, Dec.,**1999**.
- [2] **T.Venkateswarlu**, and C. Eswaran, “Realization Of Multidimensional Digital Transfer Functions With Separable Numerator Or Denominator Polynomials”, International Journal of Multidimensional Systems and Signal Processing, vol.10, No.2, pp.201-211, April, **1999**.
- [3] **T.Venkateswarlu**, and C. Eswaran, “Design of 2-D Pseudorotated Digital Filters With Multiple Outputs”, IEEE Trans. Circuits Sys., part II, Analog & Dig. Sig. Pro., Vol.46, No2, pp.177-179, Feb., **1999**.
- [4] **T.Venkateswarlu**, C. Eswaran and A.Antoniou, “Design Of Circularly Symmetric Two- and Three- Dimensional Digital Filters Using The Concept Of The GIC”, IEEE Proc. G Circuits Devices Sys., Vol.138, pp.523-529, Aug., **1991**.
- [5] C.Eswaran , **T.Venkateswarlu**, and A.Antoniou, “Realization Of Multidimensional GIC Digital Filters”, IEEE Trans. Circuits Syst., Vol.CAS-37, pp.685-694, June, **1990**.
- [6] C.Eswaran , **T.Venkateswarlu**, and A.Antoniou, “Realization Of Multidimensional Digital Transfer Functions” , International Journal of Multidimensional Systems and Signal Proce.,1, pp.179-198, **1990**.
- [7] **T.Venkateswarlu**, and C. Eswaran, “Minimal Multiplier Realization Of 2-D All-Pass Digital Filters”, IEEE Trans. Circuits Syst., Vol.CAS-37, pp.670-672, May, 1990.
- [8] **T.Venkateswarlu**, and C. Eswaran, “Realization Of 2D Analogue Filters”, IEEE Electronics Letters, Vol.25, pp.1647-1648, Nov., **1989**